

IN THE CLAIMS:

1. (Currently Amended) An on-screen display apparatus ~~which holds~~ comprising a voltage holding means for holding a voltage value at a time when an input chroma signal is a null signal and ~~outputs~~ is for outputting the voltage value during an on-screen display period.

2. (Currently Amended) An on-screen display apparatus ~~which holds~~ comprising a voltage holding means for holding a voltage value at a time when an input chroma signal is a null signal and ~~outputs~~ means for generating and outputting a chroma signal ~~generated on the basis~~ as a function of the voltage value during an on-screen display period.

3. (Currently Amended) An on-screen display apparatus comprising:

a voltage holding means for holding a voltage value at a time when an input chroma signal is a null signal; and

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an output switch for outputting ~~the~~a voltage value held by the voltage holding means during an on-screen display period and outputting the input chroma signal at a time other than during the on-screen display period.

4. (Currently Amended) An on-screen display apparatus comprising:

a voltage holding means for holding a voltage value at a time when an input chroma signal is a null signal;

an AC component generation means for generating AC components of the chroma signal;

an adder for adding ~~the~~a voltage value held by the voltage holding means and the AC components of the chroma signal which are generated by the AC component generation means; and

an output switch for outputting ~~the~~a signal added by the adder during an on-screen display period and outputting the input chroma signal at a time other than during the on-screen display period.

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5. (Currently Amended) The on-screen display apparatus of Claim ~~3-3~~, wherein

the voltage holding means ~~has~~ comprises a capacitor for holding a voltage value.

6. (Currently Amended) The on-screen display apparatus of Claim ~~5-5~~, wherein

the voltage holding means further ~~has~~ comprises a resistor ~~placed~~ located on a chroma signal input side of the capacitor.

7. (Currently Amended) The on-screen display apparatus of Claim ~~6-6~~, wherein

the voltage holding means is ~~placed~~ located on a chroma signal input side of the resistor, and further ~~has~~ comprises a hold timing switch that ~~is brought into conduction~~ conducts a input chroma signal when the input chroma signal is a null signal.

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8. (Currently Amended) The on-screen display apparatus of Claim ~~6-6~~, wherein

the voltage holding means is ~~placed~~ located between the capacitor and the resistor, and further ~~has~~ comprises a hold timing switch that ~~is brought into conduction~~ conducts a input chroma signal when the input chroma signal is a null signal.

9. (Currently Amended) The on-screen display apparatus of Claim ~~3-3~~, wherein

the voltage holding means comprises:

an AD converter for converting an input chroma signal into a digital signal when the input chroma signal is a null signal;

a storage means for storing a voltage value at the time when the input chroma signal is a null signal, which has been converted into a digital signal by the AD converter; and

a DA converter for converting the voltage value stored in the storage means into an analog signal.

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10. (Currently Amended) The on-screen display apparatus of Claim 3-3, wherein

the voltage holding means ~~holds~~ is for holding the voltage value during a horizontal sync period in which the input chroma signal is a null signal.

11. (Currently Amended) The on-screen display apparatus of Claim 3-3, wherein

the voltage holding means ~~holds~~ is for holding the voltage value during a vertical sync period in which the input chroma signal is a null signal.